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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kiran Ganesh et al.

Title: 2-DIMENSIONAL PLACEMENT WITH RELIABILITY CONSTRAINTS FOR VLSI DESIGN

Docket No.: 884.141US1

Filed: November 1, 1999

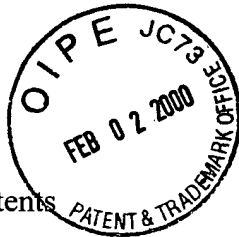
Examiner: Unknown

Serial No.: 09/431,477

Due Date: N/A

Group Art Unit: 2768

Assistant Commissioner for Patents  
Washington, D.C. 20231



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We are transmitting herewith the attached:

☒ Communication Re: Incorrect Corrected Filing Receipt (1 pg.)

☒ Copy of Corrected Filing Receipt (1 pg.)

☒ A return postcard.

☒ Copy of the first page of the application (1 pg.)

No Additional fee is required.

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this Transmittal Letter and the paper, as described above, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on this 28 day of January, 2000.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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By: *Ann M. McCrackin*

Name: Ann M. McCrackin

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AMM1:CMG:jaa

S/N 09/431,477

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kiran Ganesh et al.

Serial No.: 09/431,477

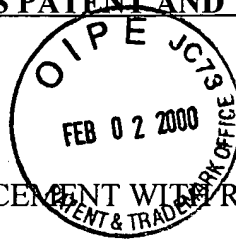
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Title: 2-DIMENSIONAL PLACEMENT WITH RELIABILITY CONSTRAINTS FOR VLSI DESIGN

Examiner: Unknown

Group Art Unit: 2768

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COMMUNICATION RE: INCORRECT CORRECTED FILING RECEIPT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Applicants hereby request correction of the Corrected Filing Receipt with respect to the above-identified patent application. In the Corrected Filing Receipt received November 29, 1999, (copy enclosed), the Title is still incorrect and should be corrected to read, 2-DIMENSIONAL PLACEMENT WITH RELIABILITY CONSTRAINTS FOR VLSI DESIGN as evidenced by the first page of the application as filed (copy enclosed).

Applicants would appreciate the above-identified printing error be corrected and that a new "corrected" filing receipt be sent to Applicants' representatives at the address given below.

Respectfully submitted,

KIRAN GANESH ET AL.

By their Representatives,

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Date Jan. 28, 2000

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231,

on

January 28, 2000  
Date of Deposit

Jason Anderson  
Jason Anderson

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FILING RECEIPT

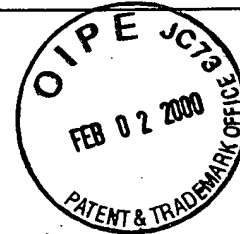
CORRECTED



UNITED STATES DEPARTMENT OF COMMERCE  
Patent and Trademark Office  
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTORNEY DOCKET NO.	DRWGS	TOT CL	IND CL
09/431,477	11/01/99	2768	\$1,400.00	884.141US1	14	31	7

021186  
SCHWEGMAN LUNDBERG WOESSNER & KLUTH  
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Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts of Application" ("Missing Parts Notice") in this application, please submit any corrections to this Filing Receipt with your reply to the "Missing Parts Notice." When the PTO processes the reply to the "Missing Parts Notice," the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s) KIRAN GANESH, MOUNTAIN VIEW, CA; ARTOUR LEVIN, SAN JOSE, CA; MILES F. MCCOO, HAIFA, ISRAEL; NARESH K. SHAGAL, SANTA CLARA, CA.

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IF REQUIRED, FOREIGN FILING LICENSE GRANTED 11/24/99  
TITLE

2-DIMENSIONAL PLACEMENT WITH RELIABILITY CONSTRAINTS FOR VLSI DESIGN

PRELIMINARY CLASS: 395

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Schwegman, Lundberg,  
Woessner & Kluth, P.A.

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3-DIMENSIONAL PLACEMENT WITH RELIABILITY CONSTRAINTS FOR VLSI DESIGN  
CLASS 395  
TE REGISTRATION NUMBER 11/01/99 09/431,477

DATA ENTRY BY: WASHINGTON, LINDA TEAM: 06 DATE: 01/12/00

(See reverse for new important information)

UNITED STATES PATENT APPLICATION



**2-Dimensional Placement With Reliability Constraints for VLSI Design**

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Client Reference P6788

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